









Email: principalengg_ytc@yes.edu.in Call: 02162-271238/39 Mob. 9172220775 **Faculty of Engineering**

Department of Electronics and Telecommunication

Academic Year 2023-24

Semester -III (Second Year)

Structure of Course

Semester III Course Code Course Title Teaching Evaluation Scheme													
Course Category	Course Code	Course Title		ching eme		E	Credit						
Category			L	T	P	CA	MSE	ESE	Total				
BSC	BTBS301	Engineering Mathematics – III	3	1	-	20	20	60	100	4			
PCC 1	BTETC302	Electronic Devices & Circuits	3	1	-	20	20	60	100	4			
PCC 2	BTETC303	Digital Electronics	3	1	-	20	20	60	100	4			
ESC	BTES304	Electrical Machines and Instruments	3	1	-	20	20	60	100	4			
LC	BTETL305	Electronic Devices & Circuits Lab	-	-	2	60	-	40	100	1			
LC	BTETL306	Digital Electronics Lab	-	-	2	60	-	40	100	1			
Seminar	BTETS307	Seminar I	-	-	4	60	-	40	100	2			
Internship	BTES211P	Internship – 1 Evaluation	-	-	-	-	-	-	-	Audit			
		Total	12	4	8	260	80	360	700	20			

Vision:

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Mission:

M1: To provide cutting edge platform to explore innovative, creative and entrepreneurial leadership qualities among the students.

M2: To be hungry for academic excellence through innovative procedure.

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Department of Electronics and Telecommunication

Course Title: Engineering Mathematics III

BTBS301_1	Solve higher order linear differential equation using appropriate techniques for modeling and analyzing electrical circuits.
BTBS301_2	Solve problems related to Fourier transform, Laplace transform and applications to Communication systems and Signal processing.
BTBS301_3	Obtain Interpolating polynomials, numerically differentiate and integrate functions, numerical solutions of differential equations using single step and multi-step iterative methods used in modern scientific computing.
BTBS301_4	Perform vector differentiation and integration, analyze the vector fields and apply to Electromagnetic fields.
BTBS301_5	Analyze conformal mappings, transformations and perform contour integration of complex functions in the study of electrostatics and signal processing.

CO to PO Mapping	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTBS301_ 1	2					2				2			2		2
BTBS301_ 2	3					2				2			2		2
BTBS301_ 3	3					2				2			2		2
BTBS301_ 4	3					2				2			2		2
BTBS301_ 5	3					2				2			2		2
Avg.	2.8					2				2			2		2

Correlation level defined 1,2,3 as below

1-Slight 2-Modrate 3-High

Course Coordinator

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Department of Electronics and Telecommunication

Course Title: EDC

BTETC302_1	Comply and verify parameters after exciting devices by any stated method.
BTETC302_2	Implement circuit and test the performance.
BTETC302_3	Analyze BJT, JFET and MOSFET for various applications.
BTETC302_4	Analyze Feedback amplifiers and oscillators.

CO to PO Mapping	PO1	PO2	РОЗ	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTETC3 02_1	3	3	2	2	2				1	1	2	2	2	1	0
	3	3	Z						Т	1	2	2		ı	2
BTETC3															
02_2	3	3	2	2	1				1	1	1	1	2	1	2
BTETC3															
02_3	3	2	2	2	2				1	1	2	2	2	1	2
BTETC3															_
02_4	3	3	3	2	2				2	2	2	2	2	1	2
AVG	3	2.75	2.25	2	1.75				1.25	1.25	1.75	1.75	2	1	2

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Department of Electronics and Telecommunication

Course Title: DE

BTETC303_1	Use the basic logic gates and various reduction techniques of digital logic circuit in detail.
BTETC303_2	Design combinational and sequential circuits.
BTETC303_3	Design and implement hardware circuit to test performance and application.
BTETC303_4	Understand the architecture and use of VHDL for basic operations and
	Simulate using simulation software.

CO to PO Mapping	PO 1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTETC3 03_1	2	2	2									2	1	1	1
BTETC3 03_2		2		1								2	1	1	1
BTETC3 03_3	2	1	1	1								2	1	1	1
BTETC3 03_4	1			2								2	1	2	1
AVG	1.6 7	1.5	1.5	1.5								2	1	1.2	1

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Department of Electronics and Telecommunication

Course Title: EMI

BTES304_1	The ability to formulate and then analyze the working of any electrical machine using mathematical model under loaded and unloaded conditions.
BTES304_2	The skill to analyze the response of any electrical machine.
BTES304_3	The ability to troubleshoot the operation of an electrical machine.
BTES304_4	The ability to select a suitable measuring instrument for a given application.
BTES304_5	The ability to estimate and correct deviations in measurements due to the influence of the instrument and due to the accuracy of the instrument.

CO to PO Mapping	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTES304 _1	2	2	2									2	1	1	1
BTES304 _2		2		1								2	1	1	1
BTES304 _3	2	1	1	1								2	1	1	1
BTES304 _4	1			2								2	1	2	1
BTES304 _5		1		2								2	1	1	1
AVG	1.6 7	1.5	1.5	1.5								2	1	1.2	1

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Academic Year 2023-24

Semester –IV (Second Year)

Structure of Course

Course Category	Course Code	Course Title		ching		E	valuatio	on Schen	ne	Credit
			L	T	P	CA	MSE	ESE	Total	
PCC 3	BTETC401	Network Theory	3	1	-	20	20	60	100	4
PCC 4	BTETC402	Signals and Systems	3	1	-	20	20	60	100	4
HSSMC	BTHM403	Basic Human Rights	3	-	-	20	20	60	100	3
BSC	BTBS404	Probability Theory and Random Processes	3	-	-	20	20	60	100	3
PEC 1	BTETL406	(A) Numerical Methods and Computer Programming (B) Data Compression & Encryption (C) Computer Organization and Architecture (D) Introduction to MEMS (E) Python Programming Network Theory Lab & Signals and	3	1	4	20	20	40	100	2
		Systems Lab								
Seminar	BTETS407	Seminar II	-	-	4	60	-	40	100	2
Internship	BTETP408 (Internship – 2)	Field Training /Internship/Industrial Training (minimum of 4 weeks which can be completed partially in third semester and fourth semester or in at onetime).	-	-	-	-	-	-	-	Audit (evalua t ion will be in V Sem.)
	•	Total	15	3	8	220	100	380	700	22

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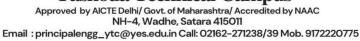
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Yashoda Technical Campus





Faculty of Engineering

Department of Electronics and Telecommunication

Course Title: Network Theory

BTETC401_1	Apply knowledge of mathematics to solve numerical based on network simplification and it will be used to analyze thesame.
BTETC401_2	Design passive filters and attenuators theoretically and practically. To apply knowledge for design of active filters as well as digital filters and even extend this to advance adaptive filters.
BTETC401_3	Identify issues related to transmission of signals, analyze different RLC networks.
BTETC401_4	Find technology recognition for the benefit of the society.

CO to PO Mapping	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTETC401												2			
_1	2	2											2		
BTETC401												2			
2	1	1	2										2		1
BTETC401												2			
_3		1		1									2	1	1
BTETC401												2			
_4		1				1							2	2	1
AVG	1.5	1.2 5	2	1		1						2	2	1.5	1

Correlation level defined 1,2,3 as below

1-Slight 2-Modrate 3-High

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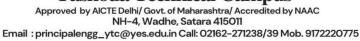
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Yashoda Technical Campus





Faculty of Engineering

Department of Electronics and Telecommunication

Course Title: BHR

BTHM403_1	Students will be able to understand the history of human rights
BTHM403_2	Students will learn to respect others caste, religion, region and culture.
BTHM403_3	Students will be aware of their rights as Indian citizen.
BTHM403_4	Students will be able to understand the importance of groups and
	communities in the society.

CO to PO Mapping	P O 1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTHM403_1	2	2	2	2								2	2		
BTHM403_2			2	2								2	2		1
BTHM403_3	2		2	2								2	2	1	1
BTHM403_4		2		2								1	2	2	1
AVG	2	2	2	2								1.75	2	1.5	1

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Department of Electronics and Telecommunication

Course Title: PTRP

BTBS404 _1	Understand representation of random signals
BTBS404 _2	Investigate characteristics of random processes
BTBS404_3	Make use of theorems related to random signals
BTBS404 _4	To understand propagation of random signals in LTI systems

CO to PO Mapping	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
BTBS404	2	2	2	2								2			
_1													2		
BTBS404			2	2								2			
_2													2		1
BTBS404	2		2	2								2			
_3													2	1	1
BTBS404		2		2								1			
_4													2	2	1
AVG	2	2.00	2	2.0 0								2	2	1	1

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