# FACULTY -BIODATA

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|  **Department** | Electronics And Telecommunication Engineering  | C:\Users\admin\Downloads\Bshailaja (2).JPG |
| **Designation:**  | Assistant Professor |
| **Name of Faculty:** | Mrs.Pandit Vaishnavi Avadhoot  |
| **Date of Birth:**  | 24/04/1984 | **Date of Joining**: |  |
| **Qualification with Class/Grade** | **UG** | **PG** | **Ph.D.** |
| **First Class** | **First Class** |  |
| **Area of Specialization:**  | Electronics(VLSI) |
| **Total Experience in Years**:  | **Teaching** | **Industry** | **Research** |
| 7 | 2.5 | 00 |
| **Mobile No:**  | 9373208752 | **E-mail ID:** | Vaishnavipandit\_engg@yes.edu.in |
| **Number of PhD, M.Tech , B.Tech Project Guided** | **UG:** 3 | **PG:**  | **Ph.D. :** |
| **Professional Society Memberships** | LMISTE  |
| **Paper Published in Journals** | National: 2 | International: 2 |
| **Paper Presented in Conferences** | National:  | International: 2 |
|  **Books/Chapters/ Patents / Copy rights Published** | Books: - 01 | Chapters:-  | Patents:  | Copyrights: - |
| **STTPs, FDPs, Workshops attended**  | STTPs:  | FDPs: 2 | Workshops: 12  |
| Webinars & Seminars attended | Webinars:  | Seminars: 3 |
| **STTP, FDP,Webinar& Seminar conducted** | STTP:  | FDP:2 | Seminar: | Webinar:  |
| **Resource Person Work Details**  | PCB Design Workshop |
| **NPTEL/Swayam/NITTR/MOOC/ Other courses**  | Accreditation and Outcome Based Learning |
| **Awards/Recognitions** | Got Recognition from Texas Instruments Student Competition  |
| **Consultancy Activities** |  |
| **Google Scholar Link** | <https://scholar.google.com/citations?hl=en&user=BAyEYuAAAAAJ> |
| **IEEE Author Profile ID:**  | <https://ieeexplore.ieee.org/author/814740805969764> |
| **Google Site/Website link**  | <https://sites.google.com/yes.edu.in/profvedpathakshailaja/bio> |

 **Name & Signature of Faculty**